



2025 June Intake

Y2 – S1 Class Timetable

(1st July 2026) to (9th October 2026)

Semester Final Written Examinations: 21st and 22nd October 2026

Start Time	Monday	Tuesday	Wednesday	Thursday	Friday
9.30 am-10.30 am	EM III (Lab) Mr. Jerith Electronic Lab		LEDE (Lec) Mr. Benorith 12 th Floor Hall 03		
10.30 am-11.30 am					
11.30 am-12.30 pm		MP II Mr. Jerith Electrical Lab			
12.30 pm-1.00 pm	LUNCH BREAK				
1.00 pm-2.00 pm	EM III (Lec) Mr. Jerith 12 th Floor Hall 03	EM III + LEDE (T) 12 th Floor Hall 03	LCSA (Lec) Mr. Ahula Seneviratne 12 th Floor Hall 03	ESAE (Lec+ Lab) Mr. Dilanka Electrical Lab	
2.00 pm-3.00 pm					
3.00 pm-4.00 pm	LEDE (Lab) Mr. Benorith Electronic Lab	EE Club/Student Counsellor Meetings	LCSA (Lab) Mr. Ahula Seneviratne Electronic Lab		
4.00 pm-5.00 pm					

EE060201 - Engineering Mathematics III (**EM III**)

EE060202 - Embedded Systems Programming and Applications in the Environment (**ESAE**)

EE060203 - Linear Electronics Design & Environment (**LEDE**)

EE060204 - Local Communications Systems and Applications (**LCSA**)

EE060205 - Mini-Project II (**MP II**)