



2026 June Intake

Y1 – S1 Class Timetable

(13th July 2026) to (16th October 2026)

Semester Final Written Examinations: 26th and 27th October 2026

Start Time	Monday	Tuesday	Wednesday	Thursday	Friday
9.30 am-10.30 am	EM I (Lec) Mr. Shuhaib 12 th Floor Hall 03	DE (Lec) Prof. Dayawansa 12 th Floor Hall 03	ECA + DE (T) Electrical Lab	PF (Lec + Lab) Mr. Benorith Electronic Lab	ECA (Lec) Mr. Benorith Electrical Lab
10.30 am-11.30 am					
11.30 am-12.30 pm					
12.30 pm-1.00 pm	LUNCH BREAK				
1.00 pm-2.00 pm	ECA (Lab) Mr. Benorith Electronic Lab		DE (Lab) Mr. Dilanka Electronic Lab	PPE I (Lec + Lab) Mr. Lakshan Electronic Lab	
2.00 pm-3.00 pm					
3.00 pm-4.00 pm		EE Club/Student Counsellor Meetings			EM I (Lab) Mr. Shuhaib Electronic Lab
4.00 pm-5.00 pm					

EE060101 - Engineering Mathematics I (**EM I**)

EE060102 - Engineering Circuit Analysis (**ECA**)

EE060103 - Digital Electronics (**DE**)

EE060104 - Programming Fundamentals (**PF**)

EE060105 - Professional Practice and the Environment I (**PPE I**)