



2025 July Intake

Y1 – S1 Class Time Table

(28th July 2025) to (31st October 2025)

Semester Final Written Examinations: 13th and 14th November 2025

Start Time	Monday	Tuesday	Wednesday	Thursday	Friday
9.30 am	EM I (Lec)	ECA (Lab)	DE (Lab)	DE (Lec)	ECA (Lec)
10.30 am					
11.30 am					
BREAK					
1.00 pm		Tutorial Sessions (ECA&DE)	EM I (Lab)	PF (Lec + Lab)	PPE I (Lec + Lab)
2.00 pm					
3.00 pm					

Lecture Hall: 12th Floor Hall No 3

Laboratory: 7th Floor Electronic/Electrical Laboratory

Module Code and Title	Lecturers in Charge
EE060101 - Engineering Mathematics I	Mr. Lawrence Jerith
EE060102 - Engineering Circuit Analysis	Mr. Lawrence Benorith
EE060103 - Digital Electronics	Prof. Indra Dayawansa
EE060104 - Programming Fundamentals	Mr. Lawrence Benorith
EE060105 - Professional Practice and the Environment I	Mr. Joseph Lakshan